



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


Appl. No. : 10/501,796 Confirmation No.: 6921  
Applicant(s) : Friedrich Hapke  
Filing Date : July 16, 2004  
Title : Integrated Circuit with Self-Testing Circuit  
Group Art Unit : 2133  
Examiner : Tu, Christine Trinh Le  
Docket No. : DE02 0018 US

MAIL STOP AMENDMENT  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

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**CERTIFICATE OF MAILING (37 CFR §1.8)**

I hereby certify, pursuant to 37 CFR §1.8, that this correspondence is being facsimile transmitted or deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below:

Dated: DECEMBER 16, 2005 By:   
Ariana Sanchez

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**AMENDMENT**

SIR or MADAM:

This paper is responsive to the non-final Office Action mailed September 16, 2005, having a shortened statutory response period expiring on December 16, 2005. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Amendments to the Drawings** are detailed on page 5 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.